1995 Index
IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems
Vol.14

This index covers all technical items - papers, correspondence, reviews, etc. - that appeared in this periodical during 1995, and items from previous years that were commented upon or corrected in 1995.

The Author Index contains the primary entry for each item, listed under the first author's name, and cross-references from all coauthors. The Subject Index contains several entries for each item under appropriate subject headings, and subject cross-references.

It is always necessary to refer to the primary entry in the Author Index for the exact title, coauthors, and comments/corrections.

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FIRST ANNOUNCEMENT AND CALL FOR PAPERS

Sponsored by the ACM SIGDA, the IEEE Solid-State Circuits Council, and the IEEE Circuits and Systems Society

1996 INTERNATIONAL SYMPOSIUM ON LOW POWER ELECTRONICS AND DESIGN

Monterey Convention Center
Doubletree Hotel, Monterey, CA
August 12 - 14, 1996

The International Symposium on Low Power Electronics and Design provides a forum for the presentation of advances in low power systems and components. All aspects of designing a low power product, from fabrication technology and circuits to systems and software, are within the scope of the Symposium. Topics of interest include, but are not limited to:

Technology and Devices
- Technology and processing
- Batteries
- Displays
- Device design

Digital Electronics
- Digital circuits
- Memories
- Micro-architecture
- Energy Recovery circuits

Analog/Mixed-Signal Electronics
- Analog/mixed signal circuits
- RF techniques
- Wireless systems
- Efficient DC-DC conversion

Software and Systems
- Compilers & embedded systems
- Logic and module design
- Microprocessor design
- Power management and design

Synthesis and Design
- Behavioral & system level design aids
- Logic synthesis and physical design
- Digital circuit design
- Interconnect and package design

Simulation and Estimation
- Algorithmic and architectural level
- Circuit and gate level
- Hierarchical simulation
- Power analysis and verification

This Symposium is the result of a merger between the Symposium on Low Power Electronics and the International Symposium on Low Power Design. Like its predecessors, the merged symposium will contain a mix of invited talks and contributed papers. However, the Symposium will consist of two parallel tracks: one focusing on systems and CAD, the other focusing on circuits and technology. All invited talks will be plenary and thus can be heard by all attendees. Contributed papers should report on significant advances in the field, and make clear what new ideas and results are being described. Reduction to hardware or practical application is desirable but not necessary.

SUBMISSION OF PAPERS

In general, submitted papers must describe original work which will not be announced or published elsewhere prior to the Symposium. However, overview/review papers of appropriate topics in low power electronics and design will also be considered. Submissions may range in length from abstracts of 500-750 words with supporting figures to full length papers of up to 5000 words. Submissions longer than 1000 words should include a 100-150 word abstract summarizing what is new and important in the paper. Submissions must include information on the contact person for the paper: name, full address (including street address for express delivery), telephone number, fax number, e-mail address. Authors must indicate in which of the six topic areas listed above the paper should be considered, and should submit 15 copies of the paper. DEADLINE FOR RECEIPT OF SUBMISSIONS IS MARCH 1, 1996. Authors of accepted papers will be notified by April 26. Accepted papers will be published in the Symposium Digest; final versions of the papers, up to 6 pages of camera- ready copy, will be due Friday, May 31, 1996. Authors of accepted papers must sign a copyright release form.

Proposals for panels are requested in all areas of low power design. Proposals must describe the topic, provide a list of participants with addresses and phone numbers, including moderator and contact. Proposals should be submitted to either of the Program Co-chairs by February 16, 1996.

All papers should be submitted to Massoud Pedram, Technical Program Co-Chair.

General Co-Chairs

Jan Rabaey
EECS Dept., Cory Hall
Univ. of California
Berkeley, CA 94720
TEL: (510) 643-8206
FAX: (510) 642-2739
jan@eecs.berkeley.edu

Mark Horowitz
Stanford University
CIS Room 203
Stanford, CA 94305-4070
TEL: (415) 725-3707
FAX: (415) 725-6949
horowitz@ee.stanford.edu

Technical Program Co-Chairs

Massoud Pedram
Univ. of Southern California
Dept. of EE - Systems
Los Angeles, CA 90089-2562
TEL: (213) 740-4458
FAX: (213) 740-9803
massoud@zugros.usc.edu
(Express mail: 3740 McClintock Ave.)

Brock Barton
Texas Instruments
P.O. Box 655474,
Dallas, TX 75243
TEL: (214) 995-7097
FAX: (214) 995-6194
barton@hc.ti.com

INFORMATION AND REGISTRATION

Information on Symposium registration and hotel reservations will be included in the Advance Program. For further information on the Symposium or to be put on the mailing list, please contact one of the General Co-Chairs or:

Colleen Matteis
553 Monroe St.
Santa Clara, CA 95050
TEL: (408) 296-6883
FAX: (408) 985-8274
sigda@nextwave.com

Symposium information can also be found on the ISLPED web page: **http://ballade.cs.ucla.edu/islped96**